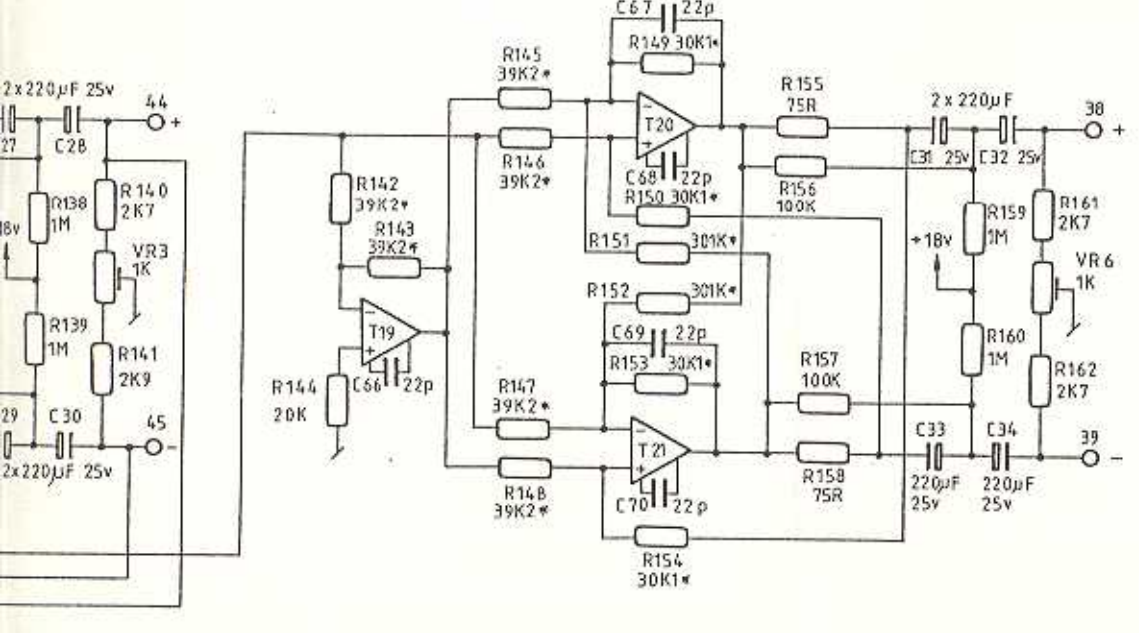
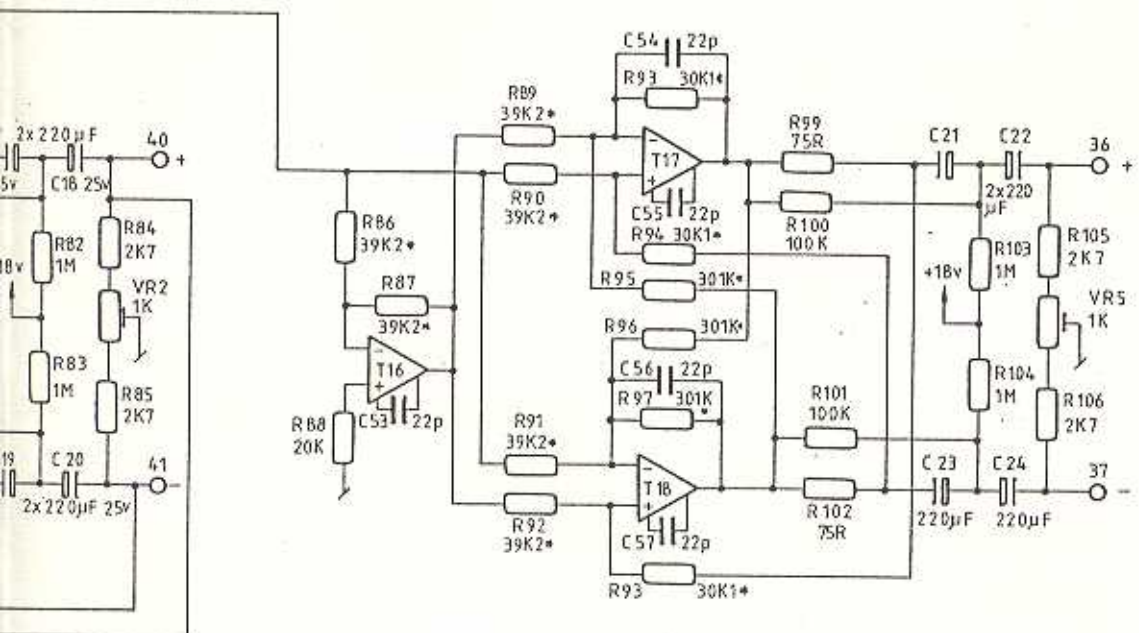
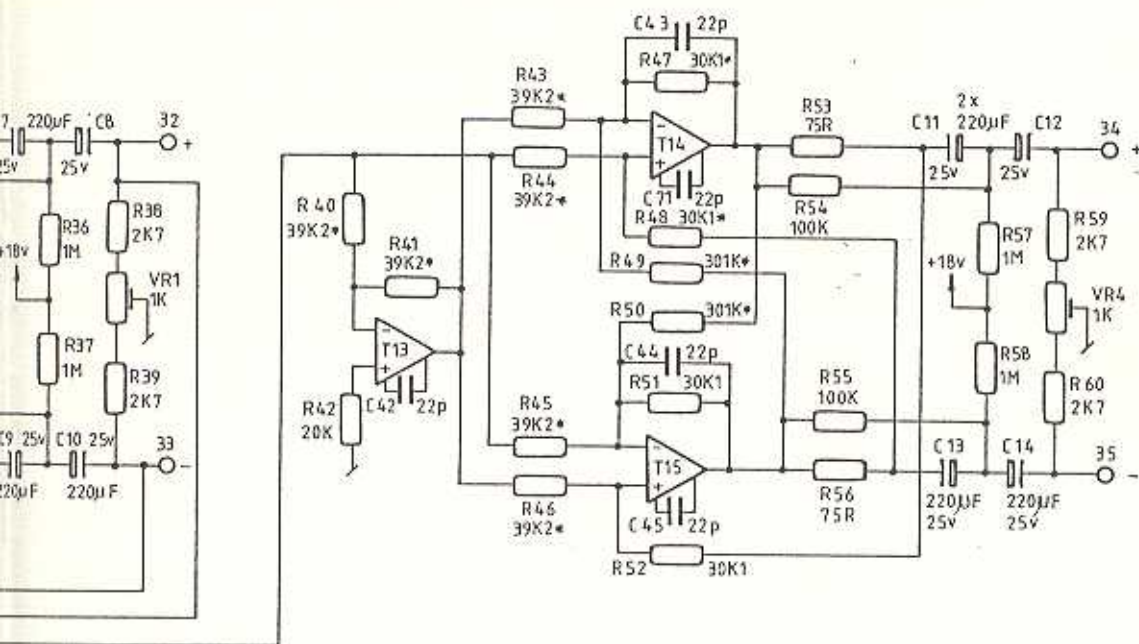


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Rev.	Date	Chkd	Details
0	5.85		REDRAWN V.6.
1	09/85		IN "1C" TOP OF R108 TO 5Q, PROGRAMME R INPUT. NOT 23, PROGRAMME R MONITOR O/P.

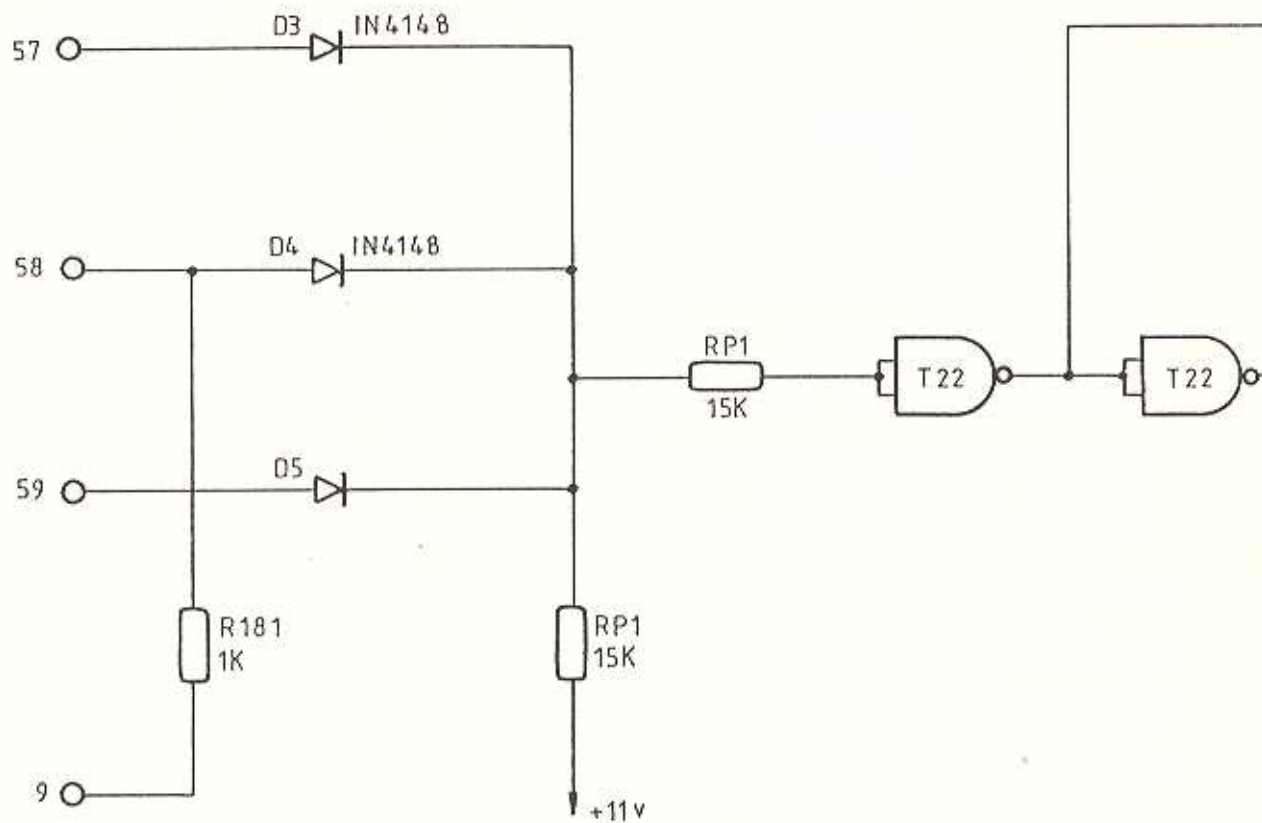


ASSY REV N° 1

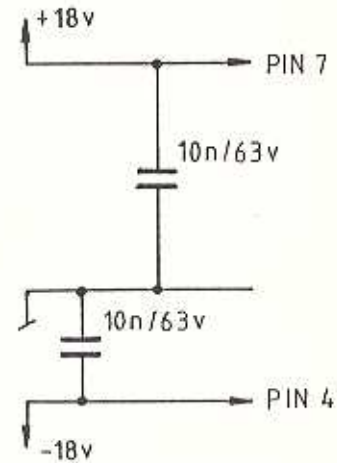
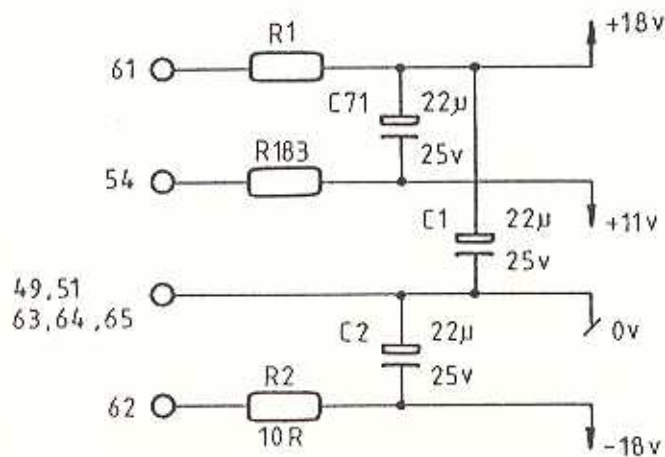
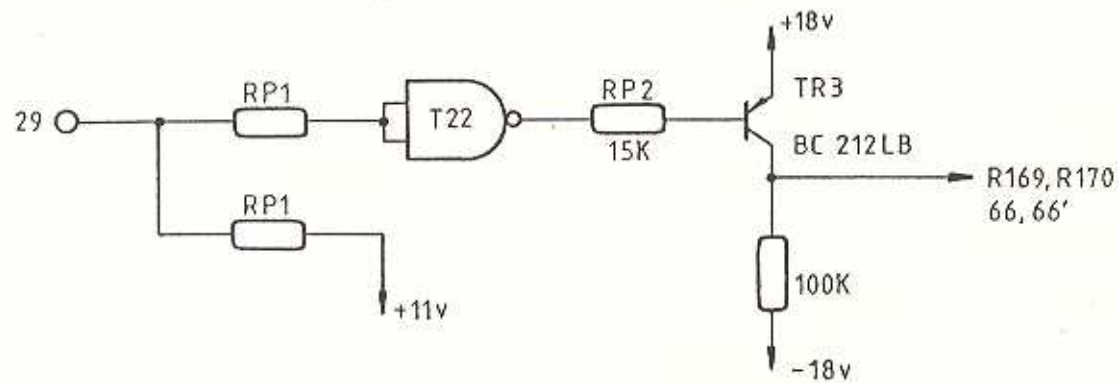
Title  
 PROGRAMME OUTPUTS  
 AND MONITOR BUFFERS

Drg. No. T82195.70

**Solid State Logic**  
 8-2T

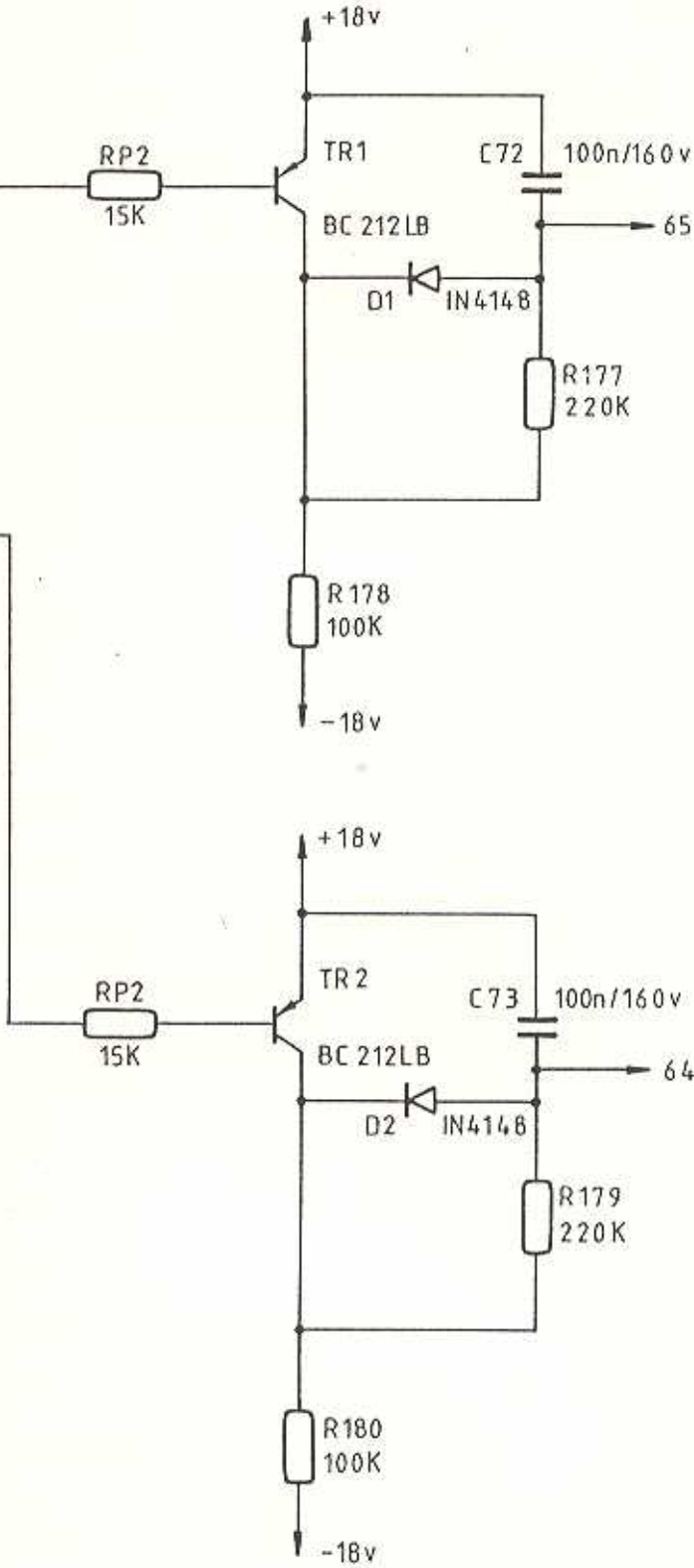


SOURCE IS MONO



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Rev.	Date	Details
0	4.84	PROTOTYPE
	6.85	REDRAWN V.B.



A  
B  
C  
D

Title	LOGIC
Drg.No.	T82195-71
<b>Solid State Logic</b>	
8-22	